Attorney Docket No.	211200US
Allomey Docker No.	21120000

First Inventor or Application Identifier Toshitada

ONE-CHIP SYSTEM LARGE-SCALE FEBRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT AND ITS PERIPHERAL CIRCUITS

Assignee Name: KABUSHIKI KAISHA TOSHIBA

72, HORIKAWA-CHO, SAIWAI-KU, KAWASAKI-SHI KANAGAWA-KEN, JAPAN Assignee Address:

	,						
	APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents	Assistant Commissioner for Patents ADDRESS TO: Box Patent Application Washington, DC 20231					
1.	Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)	ACCOMPANYING APPLICATION PARTS					
2.	■ Specification Total Sheets 20	 7. □ Assignment Papers (cover sheet & document(s)) 8. ■ Application Data Sheet. See 37 CFR 1.76 					
		9. 37 C.F.R. §3.73(b) Statement Power of Attorney					
3.	Formal Drawing(s) (35 U.S.C. 113) Total Sheets	10. □ English Translation Document (if applicable)					
		11. ■ Information Disclosure Statement (IDS)/PTO-1449 ■ Copies of IDS Citations					
4.	□ Oath or Declaration Total Pages □	12. □ Preliminary Amendment					
	a. ☐ Newly executed (original or copy)	13. White Advance Serial No. Postcard					
	b. Copy from a prior application (37 C.F.R, §1.63(d)) (for continuation / divisional w/ box 17 completed)	14. Certified Copy of Priority Document(s) (1) (if foreign priority is claimed)					
;= <u>=</u>	 i. DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b). 	15. □ Applicant claims small entity status.					
	□ CD-ROM or CD-R in duplicate, large table or Computer Program (<i>Appendix</i>)	16. ■ Other: Notice of Priority, List of Inventor's Name and Address					
 6.	 Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary) 						
	a. Computer Readable Form (CRF)						
.□	b. Specification or Sequence Listing on:						
- L [1]	i. ☐ CD-ROM or CD-R (2 copies); or						
er. El	ii. 🗆 Paper						
	c. Statements verifying identity of above copies	· ·					
.17. ⊩#	If a CONTINUING APPLICATION, check appropriate box, and sup	oply the requisite information below:					
	□ Continuation □ Divisional □ Continuation	-in-part (CIP) of prior application no.:					
e E	Prior application information: Examiner:	Group Art Unit:					
For C consi when	CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the pridered a part of the accompanying continuation or divisional application and it a portion has been inadvertently omitted from the submitted application part	for application, from which an oath or declaration is supplied under Box 4b, is shereby incorporated by reference. The incorporation can only be relied upon s.					
18.	Amend the specification by inserting before the first line the se	entence:					
	This application is a Continuation Division	□ Continuation-in-part (CIP)					
	of application Serial No. Filed on Which was published in English						
	Which was not published in English						
	This application claims priority of provisional application Series	al No. Filed					
	19. CORRESPOND	DENCE ADDRESS					
22850							
	(703) 413-3000 FACSIMILE: (703) 413-2220						

Name:	Marvin J. Spivak	Registration No.:	24,913
Signature:	6mm MEyen	Date:	7/10/01
Name:	C. Irvin McClelland Flegistration Number 21,124	Registration No.:	

Docket No.

211200US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Toshitada SAITO

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

ONE-CHIP SYSTEM LARGE-SCALE INTEGRATED CIRCUIT INCLUDING PROCESSOR CIRCUIT

AND ITS PERIPHERAL CIRCUITS

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	12 - 20 =	0	× \$18 =	\$0.00
INDEPENDENT CLAIMS	1 - 3 =	0	× \$80 =	\$0.00
☐ MULTIPLE DEPENDENT CLAIMS (If applicable) + \$270 =				\$0.00
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TOTAL OF ABOVE CALCULATIONS REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
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Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

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